

BUS MONITORING SYSTEM (HARDWARE PART)

NASRUNG BIN NURBA

This thesis is submitted as partial fulfillment of the requirements for the award of the
Bachelor of Electrical Engineering (Control & Instrumentation)

Faculty of Electrical & Electronics Engineering
Universiti Malaysia Pahang

MAY, 2008

ACKNOWLEDGEMENT

There are several people who contribute in preparing this thesis together with me. There are academicians, non-academicians and students. First of all, I wish to express my sincere gratitude to my supervisor, Mr. Raja Taufika Bin Raja Ismail for all his advices, guidance, motivation and supports throughout the whole project. As supervisor, he had done great job in ensuring the project reached its objectives.

Also not forgetting all the non-academicians in the FKEE laboratory who continuously giving the supports and concerns for this project. I will never forget the experience and the skills I have gathered from all of you from the very beginning of this project until it is completed. To University Malaysia Pahang (UMP), I am grateful that this institution really wanted to see its students to be skillful and knowledgeable. The funding supplied for this project is very much appreciated. I cannot return the money, but I return the victory I have achieved with this project.

Last but not least, I want to thank all my colleagues for their supports especially Kim Chin Seong and Steve. Without their warmth concern and kindness, it will be difficult for me in the programming stage at the beginning of this project. For those I did not mention here, it is not that I forgot, but the space is limited. Thank you for everything.

ABSTRACT

Vehicle monitoring system has become very famous method in engineering field these days to monitor any types of break down, vehicle behavior, fuel consumption including safety and health and many more. Bus Monitoring System was the other types of vehicle monitoring system and categorized in public transport monitoring system. The purpose of this project is to monitor the movement of public bus in the road using RF communication system. Along the way of the destination of the bus, it will arrive at few bus stops. Each time the buses reach the bus stop the driver will send the bus ID to the bus stop using RF Transmitter. Then it will help the officer to record the bus ID from the bus stop and ensure that the bus follow the right destination. In order to make this process succeed, each bus stop must have its own RF receiver to collect the bus ID that was sends from the bus. Hence, the circuit design must consist of RF module, PIC microchip, matrix keypad and also LCD to display the output. There will be two hardware constructed for this project which called transmitter board and receiver board. Transmitter board will be plugged in inside the bus and receiver board is placed at the bus stop in a safe area. Both hardware must have main control panel such as microcontroller or other microprocessors to ensure all the process in both side run smoothly.

ABSTRAK

Sistem pemantauan kenderaan telah menjadi sangat popular dan merupakan antara kaedah utama yang di gunakan dalam bidang kejuruteraan untuk memantau sebarang masalah ataupun kerosakan yg berlaku pada kenderaan, kehabisan bahan api dan juga di gunakan untuk keselamatan seperti pemantauan kondisi kenderaan sama ada berstatus baik ataupun tidak. Selain itu banyak lagi aplikasi lain yang menggunakan kaedah ini. Pemantaun pergerakan bas awam adalah salah satu kaedah yang di gunakan oleh syarikat-syarikat bas pada masa kini untuk memantau pergerakan bus mereka di dalam perjalanan. Caranya adalah semua pemandu bas perlu menghantar ID bas mereka ke setiap perhentian bus yang mereka lalui sepanjang perjalanan menuju ke destinasi terakhir . Cara ini dilaksanakan menggunakan sistem perhubungan frekuensi radio. Oleh itu setiap bas perlu mempunyai litar lengkap mengandungi ‘RF Transmitter’ untuk menghantar data ke bus stop. Pegawai di perhentian bas pula akan menerima data tersebut menggunakan ‘RF receiver’ dan ini sekaligus membolehkan mereka memastikan perjalanan bus tidak meghadapai masalah dan menuju ke destinasi yang tepat. Oleh itu, dalam menjayakan projek ini, dua jenis papan yg mempunyai litar lengkap akan di hasilkan yang juga boleh dikenali sebagai litar penghantar data dan juga litar penerimaan data. Litar penghantar data akan di letakkan berdekatan dengan pemandu bas di dalam setiap bas awam. Ini disebabkan litar tersebut khas untuk penggunaan pemandu bas sahaja dengan tujuan menghanr ID basnya ke perhentian bus yang berdekatan. Setiap perhentian bus pula dilengkapi dengan litar penerimaan data untuk tujuan menerima ID bas yg dihantar tadi. Kedua-dua litar akan di lengkapi dengan skrin paparan digital atau pun lebih dekenali sebagai LCD untuk mempamerkan ID bas.

CHAPTER 1

INTRODUCTION

1.1 Introduction

As time goes by, the improvement in technology growth larger, advance and more sophisticated. Long time ago, we never heard about bus monitoring system or vehicle monitoring system where the vehicle can be detected by using wireless technologies application, GSM or using radio frequency technologies and people also can't imagine how the process work. But in this millennium era, it's become reality when one of the largest public transport company in London using all of this technologies to provide the best possible service to their passengers. At the other place, a group of expert people make a research to implement the use of radio frequency in monitoring the bus movement. The idea was tested and developed at the University of Virginia bus route using student volunteers and the outcome result is very good where each bus stop can detect the movement of the bus by using RFID. It was another type of radio frequency technologies in the world.

As we know, radio frequencies refer to the frequencies that fall within the electromagnetic spectrum associated with radio wave propagation. When applied to an antenna, RF current creates electromagnetic fields that propagate the applied signal through space. Any RF field has a wavelength that is inversely proportional to the frequency and this means that the frequency of an RF signal is inversely proportional to the wavelength of the field.

It goes the same with this project that used the application of radio frequencies by using RF transmitter and RF receiver to monitor the movement of the bus from the bus stop. The whole process that was described here is focus only on hardware part. It was known that when the buses start moving, it will reach many bus stops along the route. So, in order to ensure that the bus already arrived at the bus stop is the RF receiver at the bus stop must be able to receive the data that was transmitted from the bus which is the ID of the bus. Thus, the system that can fulfill the requirement of this project is the circuit installed at bus must consist of four main component which is keypad to insert the bus ID, PIC16F877A that can use to analyze the data and of course RF transmitter which is the function is to transmit the data to the bus stop and finally the LCD use to display the bus ID that was insert by a bus driver. At the other side, the circuit at the bus stop must have RF receiver to receive the data and PIC to analyzing the data which received and then communicate with LCD to display the data.

1.2 Problem statement

Public bus often moves in the wrong direction and it's difficult for officer from bus company to detect this problem without implementing monitoring system. Hence, this project will contribute by providing few alternatives to the bus officer to find the

solution. It also known that there must be a data transfer from buses to the bus stop for this project.

Due to the process of sending and receiving data there must be a transmitter and receiver to complete the task. Beside that, a PIC needs to be used to store and process the data then ordered LCD to display the data at the bus stop.

1.3 Objectives

There are several objectives of this project which are:-

- I. To make sure the bus ID that was insert using keypad displayed correctly on LCD at transmitter board inside the bus.
- II. To ensure the process of sending and receiving bus ID success using RF transmitter and RF receiver.

1.4 Project scope

- I. Transmitter board will be installed inside the bus and receiver board will be plugged in at bus stop.
- II. Each time bus arriving at the bus stop, bus driver will send the bus ID using RF transmitter to the bus Stop.

- III. Receiver board will receive the ID using RF receiver .Then LCD will display the bus ID after get command from PIC16F877A.

1.5 Thesis Outline

Hardware part of bus monitoring system final thesis consist of 6 chapters that explain different part of the project. Each chapter elaborates all part of hardware and software about this chapter. Actually the content also consist of information about the project and the component used as illustrate in literature review.

Chapter 1 explains the introduction about this research where all the objectives and problems that lead to the implementation of this research are stated. The chapter starts with general information of radio frequency communication and the project background.

Chapter 2 explains the literature study regarding bus monitoring system project based on recent journals and papers. The information also comes from few resources in internet that can be trusted. Generally, most of the literature discuss about project module from the basic concept to its application to this project and engineering fields.

Chapter 3 will be more focus on hardware used in bus monitoring system project. Each module has its own connection and condition which need to put in consideration during the hardware constructing. This chapter also explains all the main circuit for each component in more detail.

Chapter 4 discuss about the software development hardware. All issues regarding the project such as the transmission and receiving data process will be discussed. Flow chart is used to show the whole process of transmitting and receiving data in this chapter.

Chapter 5 provides an outline of the results obtained from the transmitter board and receiver board. Detail explanation of the result starting from the input until the LCD display the output will be further discuss in this chapter.

Chapter 6 is the last chapter and it contains the brief summary for the whole research from the beginning until it is completed. Conclusion is included as well as some recommendations for future research on bus monitoring system.

CHAPTER 2

LITERATURE REVIEW

2.1 Introduction

Inside this chapter, brief description on each project module that was used in this project will be stated. Besides that, some elaboration on fundamental of radio frequency and data transmission can be fined in this chapter.

2.2 How RF wireless connectivity work

RF is commonly used in the wireless communications industry to describe certain types of equipment which use radio frequency waves to transmit sounds and data from one point to another. In computer networking, RF is used to describe network devices such as hubs or bridge that transmits data signals using radio waves instead of data cables or telephone lines. Even though the phrase "RF wireless networking" might

seem mysterious, the underlying technology is very common. It uses radio waves, the same type of energy used to transmit radio and television broadcasting. Two-way radios and walkie-talkies also use this kind of technology.

In the middle of the radio transmission and receiving process sit two antennas in two different places which is one located at the point for transmitting the signal and the other point is for receiving the signal. In order to transmit the modulated radio signal, an electrical current will pass through the antenna inducing a magnetic field, which oscillates at the given frequency. The variations in the current create slight variations in the radio frequency [1].

Thus, we should remember that the range we get depends on terrain, obstructions, and height of antenna. Buildings can reflect RF energy making it difficult or impossible to receive the desired signal. Also, if a reflected signal is bounced off of a building or other object, it can be received along with the direct signal. If the reflected signal is out of phase with the direct signal, it is possible for the direct signal to be partially cancelled by the weaker, reflected signal. Hence, the ideal conditions for best transmission and reception signal are line of sight and outside with no obstructions.

An RF wireless communication system operating in the presence of a periodic noise environment, includes first and second wireless devices, each such device having a source of power, a transceiver coupled to the power source, for transmitting and receiving wireless information, a controller or CPU for controlling the operation of the transceiver; means for detecting and mapping the presence of the RF radiated periodic noise and means responsive to the mapped periodic noise for controlling the operation of the transceiver to communicate with the other wireless device during the quiescent periods of the radiated RF periodic noise[2].

Then the CPU will control the operation of the transceiver in response to the mapped radiated RF periodic noise to communicate with the other wireless device during the quiescent periods of the radiated RF periodic noise by enabling the transmitter to transmit when it predicts the periodic noise is in the quiescent state, thereby making the transmission process efficient.

2.3 Data transmission

Data transmission is the conveyance of any kind of information from one space to another. Historically this could be done by courier, a chain of bonfires or semaphores, and later by Morse code over copper wires.

In recent computer terms, it means sending a stream of bits or bytes from one location to another using any number of technologies, such as copper wire, optical fiber, laser, radio, or infra-red light. Practical examples include moving data from one storage device to another and accessing a website, which involves data transfer from web servers to a user's browser. A related concept to data transmission is the data transmission protocol used to make the data transfer legible. Current protocols favor packet based communication. There were two types of data transmission which is serial transmission and parallel transmission.

Serial transmission bits are sent over a single wire individually. Whilst only one bit is sent at a time, high transfer rates are possible. This can be used over longer distances as a check digit or parity bit can be sent along it easily. But for parallel transmission, multiple wires are used and transmit bits simultaneously [3]. It works

much faster than serial transmission as one byte can be sent rather than one bit. This method is used internally within the computer, for example the internal buses, and sometimes externally for such things as printers, however this method of transmission is only available over short distances as the signal will degrade and become unreadable, as there is more interference between many wires than between one.

2.4 RF fundamental

The wireless link consists of a transmitter with antenna, a transmission path and the receiver with antenna. Parameters of interest are the output power of the transmitter and the sensitivity of the receiver. Figure 2.1 illustrates the link principle.

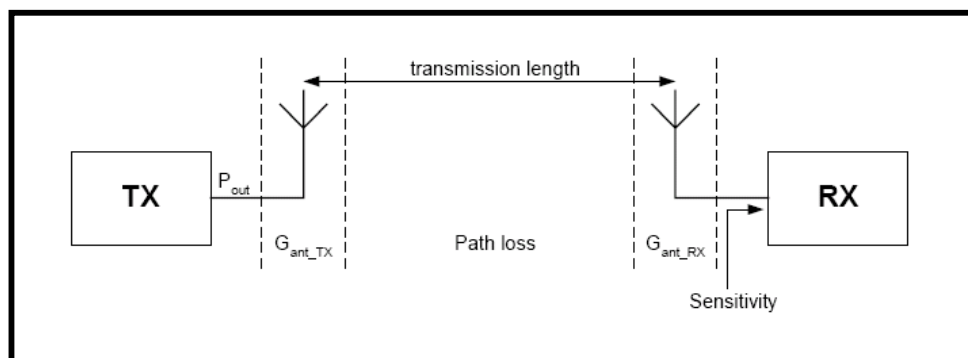


Figure 2.1 Wireless link

Sensitivity is the minimum received power that results in a satisfactory Bit Error Rate (BER, usually 1×10^{-3}) at the received data output (i.e. correct demodulation). The difference between received signal power and sensitivity is the transmission link margin also known as 'headroom'. Headroom is reduced by a number of factors such as

transmission path length, antenna efficiency, carrier frequency and physical characteristics of obstructions in the transmission path. Sensitivity and output power given in the RF-circuit datasheets are given for the load impedance which is optimal for the input LNA and the output power amplifier. This means that the impedance of the antenna used must be equal to the load stated in the datasheet, otherwise mismatch and loss of headroom occur. A typical matching network introduces in the order of 1-3 dB of attenuation.

Radio Frequency (RF) waves are lower in frequency and longer in wavelength than Infrared. At 300 MHz the wavelength is 1 m (39.37") while Consumer IR wavelengths are just under 1 millionth of a meter. Most RF remotes use a carrier in the 300-1000 MHz range.

RF receiver only needs to be tuned to the carrier frequency used by the remote. RF remotes and their receivers are tuned to a fixed frequency. The FCC allows unlicensed, low power use of 300MHz-1000MHz as well as some higher frequency bands.

As a general rule, the codes are comprised of pulses and spaces with durations of 0.3-1.5 ms which is an audible signal in the 500-2000 Hz range as shown in Figure 2.2. The IR and RF receivers output the demodulated code waveform. The only difference is that IR receivers output an active low or inverted signal while RF receivers output an active high signal as shown on Figure 2.3.

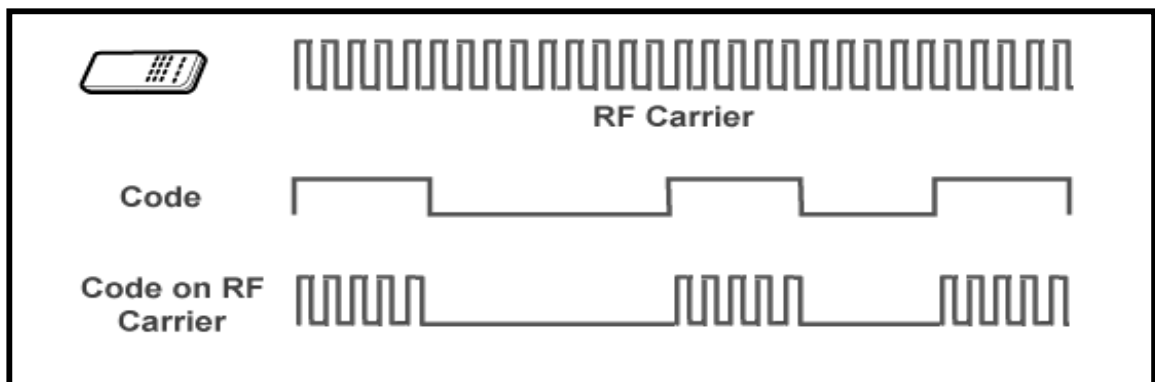


Figure 2.2 RF Carrier

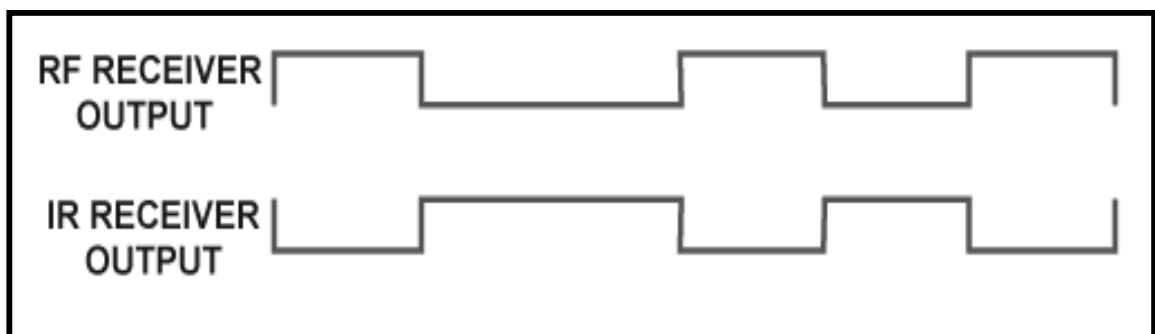


Figure 2.3 RF Receiver output & IF Receiver output

Other than range, it really makes no difference whether the data signal is used to modulate an RF carrier, an IR carrier, an ultrasonic carrier, a laser beam, or smoke. At the receiving end, the demodulated signal carries the same information. For RF control, both the transmitter and receiver need to be tuned to the same carrier frequency and need to use the same type of modulation.

Most RF remotes use ASK (Amplitude Shift Keying) or OOK (On-Off Keying). OOK is really just a special case of ASK. OOK is also called CPCA (Carrier Present, Carrier Absent). All of the illustrations above represent ASK. FSK (Frequency Shift Keying) uses two different carrier frequencies to denote two different states.

2.5 Voltage regulator

Voltage regulator ICs are available with fixed (typically 5 V, 12 V and 15 V) or variable output voltages. They are also rated by the maximum current they can pass. Negative voltage regulators are available, mainly for use in dual supplies. Most regulators include some automatic protection from excessive current (overload protection) and overheating (thermal protection).

Plenty of fixed voltage regulator ICs has three leads and looks like power transistors, such as the 7805 +5 V with 1 A regulator as shown in Figure 2.4. They include a hole for attaching a heat sink if necessary.

Voltage regulator will not stable if it work alone. Some capacitor in range $0.1 \mu\text{F}$ – $100 \mu\text{F}$ must be placed at pin 1 and pin 3. Voltage regulator will operate happily if there is a diode placed in pin 1 but it's not a compulsory. Diode can help to reduce the high voltage but it depends on the types of diode either it silicon, germanium or other models.

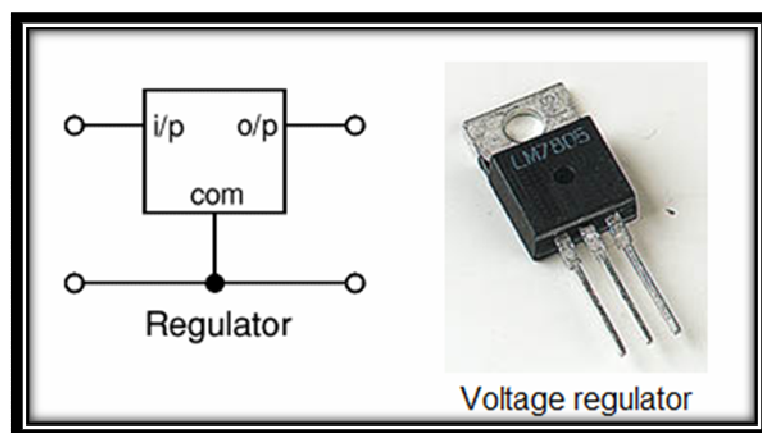


Figure 2.4 Voltage regulator

2.6 PIC16F877A

Many things should be considered before choosing a microcontroller as the controller. There is plenty of microcontrollers that was easy to find at electronics store such as ATMEL, Motorola's family and microchip product which is PIC. Basically this entire microcontroller capable to act as a controller but it's depends on the types of project that we build. It is because some of these microcontrollers have limited abilities in terms of lacking data memories and less of Input/output pins.

Amongst these microcontrollers, PIC16F877A have extra advancement. This device was build with special features such as 100,000 erase/write cycle enhanced flash program memory typical, self-reprogrammable under software control, In-Circuit Serial Programming via two pins, programmable code protection and power saving sleep mode. PIC16F87XA devices have a Watchdog Timer which can be shut-off only through configuration bits. It runs off its own RC oscillator for added reliability. There are two timers that offer necessary delays on power-up. One is the Oscillator Start-up Timer (OST), intended to keep the chip in Reset until the crystal oscillator is stable.

The other is the Power-up Timer (PWRT), which provides a fixed delay of 72 ms (nominal) on power-up only. It is designed to keep the part in reset while the power supply stabilizes. With these two timers on-chip, most applications need no external reset circuitry. Fully description of PIC16F877A can be seen at the Table 2.1 below.

Table 2.1: PIC16F877A key features

Features	PIC16F877A
Operating frequency	DC – 20MHZ
Reset and Delays	POR, BOR, (PWRT,OST)

Flash program memory (14-bit words)	8K
Data memory in bytes	368
EEPROM data memory in bytes	256
Interrupts	15
I/O ports	Ports A, B, C, D, E
Timers	3
PWM modules	2
Serial communication	MSSP, USART
Parallel communication	PSP
10-bit Analog-to-Digital module	8 input channels
Analog comparators	2
Instruction set	35 instruction
Packages	40 pin DIP

The data EEPROM and Flash program memory is readable and writable during normal operation. This memory is not directly mapped in the register file space. Instead, it is indirectly addressed through the Special Function Registers. When interfacing to the data memory block, EEDATA holds eight bits of data for read/write and EEADR holds the address of the EEPROM location being accessed.

These devices have 128 or 256 bytes of data EEPROM with an address range from 00h to FFh. On devices with 128 bytes, addresses from 80h to FFh are unimplemented and will wraparound to the beginning of data EEPROM memory. When writing to unimplemented locations, the on-chip charge pump will be turned off.

When interfacing the program memory block, the EEDATA and EEDATH registers form a two-byte word that holds the 14-bit data for read/write and the EEADR

and EEADRH registers form a two-byte word that holds the 13-bit address of the program memory location being accessed. For PIC16F876A/877A, addresses above the range of the respective device will wraparound to the beginning of program memory. The EEPROM data memory allows single-byte read and writes. The Flash program memory allows single-word reads and four-word block writes. Program memory write operations automatically perform an erase-before write on blocks of four words. A byte write in data EEPROM memory automatically erases the location and writes the new data (erase-before-write). The write time is controlled by an on-chip timer. The write/erase voltages are generated by an on-chip charge pump, rated to operate over the voltage range of the device for byte or word operations.

When the device is code-protected, the CPU may continue to read and write the data EEPROM memory. Depending on the settings of the write-protect bits, the device may or may not be able to write certain blocks of the program memory; however, reads of the program memory are allowed. When code-protected, the device programmer can no longer access data or program memory, this does NOT inhibit internal reads or writes.

2.7 Transmitter module

A transmitter is an electronic device which with the aid of an antenna propagates an electromagnetic signal such as radio, television, or other telecommunications. A transmitter usually has a power supply, an oscillator, a modulator, and amplifiers for audio frequency (AF) and radio frequency (RF). The modulator is the device modulates the signal information onto the carrier frequency, which is then broadcast.

Sometimes a device like cell phone contains both a transmitter and a radio receiver, with the combined unit referred to as a transceiver. More generally and in communications and information processing, a "transmitter" is any object or source which able to sends information to an observer or receiver. When used in this more general sense, vocal cords may also be considered an example of a "transmitter"[4].

The transmitter module in Figure 2.5 was a RF transmitter which used in this project. The RF transmitter is placed at transmitter board inside the bus. The frequency range for this types of transmitter is 433 MHz and the modulation mode is ASK or amplitude shift keying mode. The temperature maximum rating is 230° C. The actual range for data transmitting is from 100 m to 150 m but it's depends on the stability of power supply.

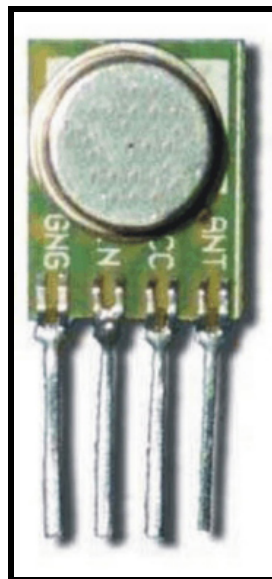


Figure 2.5 RF transmitter

Whenever possible avoid using bread-board or vary-board with RF transmitter. The long tracks inside these types of prototyping board introduce large capacitances or inductances to the circuit which can badly distort radio frequency signals. Ideally prototype or evaluation PCB should be used. Tracks connected to the antenna pin of

transmitter modules should be as short as possible. Any conductor connected to this track will act as an antenna, so it will lengthen and detune the actual antenna. The suitable length of antenna for 433 MHz RF transmitter is 16 cm and above.

2.8 Receiver module

The receiver in information theory is the receiving end of a communication channel. It receives decoded messages or information from the sender, who first encoded them. Sometimes the receiver is modeled so as to include the decoder. Real world receivers like radio receivers or telephones can not be expected to receive as much information as predicted by the theorem. The receiver is designed to work with the matching transmitter. With the addition of simple antenna the pair may be used to transfer serial data up to 200 m. The range of the system depends upon several factors, principally the type of antenna employed and the operating environment. The 200 m quoted range is a reliable operating distance over open ground using 1/4 whip antenna at both ends of the link at 1.5 meters above ground.

Smaller antenna, interference or obstacles such as building will reduce the reliable working range (down to 30 meters in extreme cases). Increased antenna height, slow data or a larger receive antenna will increase the range. The RF module frequency is from 300 MHz to 434 MHz. Its high sensitivity passive design is simple to use with a low external parts count. An ASK data shaping comparator is included [5]. RF receiver in Figure 2.6 was the other types of superheterodyne receiver. This type of receiver supports the working frequency from 315 MHz to 433 MHz.



Figure 2.6 RF Receiver

The receiving sensitivity for this RF receiver is -101 dBm and the operating voltage supply is from 3 V to 6 V. Applying SAW crystal oscillation overcomes easy frequency excursion of LC circuit was the advantages of this RF receiver. Most parts of module are integrated into the chip 3310A, less external components, stable and reliable performance, and excellent anti-jamming ability.

The output data signal is TTL and can be directly connected to decoder. The antenna must be set to 16 cm length if the operating frequency is 433 MHz. These types of receiver are quite popular around few countries in Asia because it's easy to get and the market is very wide compare to the European model of RF receiver.

The only problem of this type of receiver is the output of receiver module come with noise and also can create noise in case of special requirement, but the receiving sensitivity will be reduced. The output or RxD pin which receives a lot of noise will cause a trouble because the data that was received will not accurate. It is because noise will interfere with the data signal at receiving point. The way to troubleshoot this problem was explain in next chapter inside the designation of receiver circuit article.

2.9 Keypad module

A keypad is a convenient way of entering data into the PIC without tying up one I/O line for each switch. A four row by four column keypad consists of 16 individual push buttons, but only needs eight input/output lines to determine which of the sixteen keys has been pressed.

A 4×4 keypad consists of 16 individual push buttons and eight wires. Each switch will connect two of the eight wires together when pressed. The eight wires are arranged in a 4 ×4 wire grid patterns and when any one of the 16 push buttons is pressed, a contact is made between the wire that is associated with the switches column and the wire that is associated with the switches row. By scanning the eight wires for these connections, the button that is pressed, if any, can be determined. The types of keypad which used in this project are 4×4 matrix keypad as shown on Figure 2.7.



Figure 2.7 4×4 matrix keypad